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March 15, 2004

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Attention: Mr. Fred O. Ferris, III, Patent Examiner

Re: Patent Application Serial Number: 10/026,312
Our File Number: 44359/08329 EMB1P074

Dear Mr. Ferris:

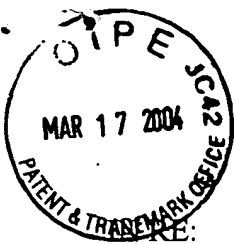
We are enclosing an Information Disclosure Statement in connection with the above-referenced application. Included with this Information Disclosure Statement are the International Search Report issued by the European Patent Office and one copy of each of the documents cited, all of which were recently received from our associate handling the prosecution of that European Patent Office counterpart to the present application. If you have any questions regarding these materials, please contact me.

Very truly yours,

C. Douglas McDonald

CDM/hmu
Enclosures

cc: Mr. Stephen Dyde (w/encl.)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:
TITLE:

Patent Application Serial No. 10/026,312
AUBURY, MATTHEW
SYSTEM, METHOD AND ARTICLE OF
MANUFACTURE FOR PROFILING AN EXECUTABLE
HARDWARE MODEL USING CALLS TO PROFILING
FUNCTIONS

#2
S. Coyle
3/29/04

FILING DATE: 12/21/2001
GROUP ART UNIT: 2128
ATTORNEY DOCKET NO.: 44359/08329 EMB1P074

INFORMATION DISCLOSURE STATEMENT

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Sir:

In accordance with 37 C.F.R. § 1.98, Applicant, through his undersigned attorney, hereby makes of record the prior art set forth on the attached form PTO-SB/08B. One copy of each item of prior art noted on this form is enclosed for consideration by the Examiner.

Respectfully submitted,

C. Douglas McDonald
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CARLTON FIELDS, P.A.
P.O. Box 3239
Tampa, FL 33601-3239
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Attorney for Applicant,
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Applicant: AUBURY, MATTHEW
Title: SYSTEM, METHOD AND ARTICLE OF
MANUFACTURE FOR PROFILING AN
EXECUTABLE HARDWARE MODEL USING
CALLS TO PROFILING FUNCTIONS

Serial No. 10/026,312
Filed: 12/21/2001

Examiner: FERRIS III, FRED O.
Attorney Docket No.: 44359/08329 EMB1P074
Information Disclosure Statement

Group Art Unit: 2128

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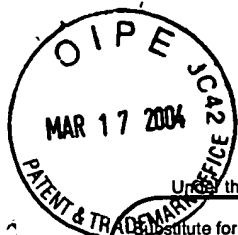
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop – Disclosure Statement, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450, on the date set forth below.

C. Douglas McDonald

3/15/04

C. Douglas McDonald, Attorney of Record

Date



PTO/SB/08B (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/026,312
Filing Date	12/21/2001
First Named Inventor	Matthew Aubury
Art Unit	2128
Examiner Name	Fred Ferris, III
Attorney Docket Number	44359/08329

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Sheet 1 of 2

NON PATENT LITERATURE DOCUMENTS

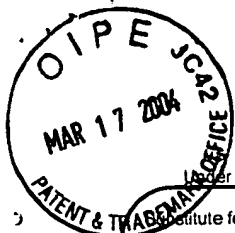
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PARKINSON M F ET AL: "Profiling in the ASP codesign environment" SYSTEM SYNTHESIS, 1995., PROCEEDINGS OF THE EIGHTH INTERNATIONAL SYMPOSIUM ON CANNES, FRANCE 13-15 SEPT.	
		1995, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 13 September 1995 (1995-09-13), pages 128-133, XP010192178 ISBN: 0-8186-7076-2 the whole document	
		HARKIN J ET AL: "Partitioning methodology for dynamically reconfigurable embedded systems" IEE PROCEEDINGS E. COMPUTERS	
		& DIGITAL TECHNIQUES, INSTITUTION OF ELECTRICAL ENGINEERS. STEVENAGE, GB, vol. 147, no. 6, 28 November 2000 (2000-11-28) pages 391-396, XP006013943 ISSN: 1350-2387 the whole document.	
		TAMMEMAE K ET AL: "AKKA: A Tool-kit for Cosynthesis and Prototyping" IEE DIGEST: COLLOQUIUM ON HARDWARE-SOFTWARE COSYNTHESIS FOR RECONFIGURABLE SYSTEMS, vol. 96, no. 036	
		February 1996 (1996-02), pages 8/1-8/8, XP002244550 the whole document	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/026,312
		Filing Date	12/21/2001
		First Named Inventor	Matthew Aubury
		Art Unit	2128
Examiner Name	Fred Ferris	Technology Center	2100
Attorney Docket Number	44359/08329 EMB1P074		
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		GREHAN R: "Code Profilers: Choosing a Tool for Analyzing Performance" METROWERKS WHITE PAPERS, 'Online! 28 April 1999 (1999-04-28), XP002244551 Retrieved from the Internet: [URL:www.metrowerks.com/pdf/CodeProfilers.pdf] 'retrieved on 2003-06-17! page 2 page 4 - page 6	
		WO D ET AL: "Compiling to the gate level for a reconfigurable co-processor" FPGAS FOR CUSTOM COMPUTING MACHINES, 1994. PROCEEDINGS, IEEE WORKSHOP ON NAPA VALLEY, CA, USA 10-13 APRIL 1994, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, 10 April 1994 (1994-04-10), pages 147-154, XP010098110 ISBN: 0-8186-5490-2 the whole document.	
		FRUMKIN M ET AL: "Trace-driven debugging of message passing programs" PARALLEL PROCESSING SYMPOSIUM, 1998. IPSP/SPDP 1998. PROCEEDINGS OF THE FIRST MERGED INTERNATIONAL... AND SYMPOSIUM ON PARALLEL AND DISTRIBUTED PROCESSING 1998 ORLANDO, FL, USA 30 MARCH-3 APRIL 1998, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 30 March 1998 (1998-03-30), pages 753-762. XP010276666 ISBN: 0-8186-8404-6 the whole document	

Examiner Signature	Date Considered
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.
This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.